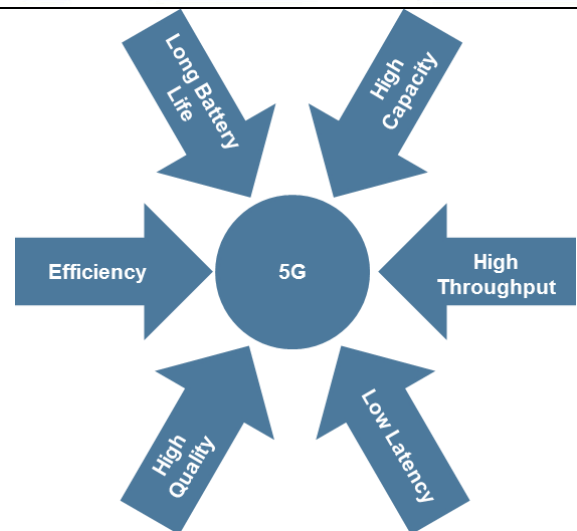


**5G MASSIVE MIMO LAB features:**

- Complete Full-Duplex System with 16 TX and 16 RX channels
- Frequency Range of 50 MHz to 3.8 GHz
- ASIC architecture with LNA, PA, I/Q Mixers, Synthesizers, RX/TX Filters and RX/TX Gain Control
- Linux GnuRadio interface
- On-board GPS disciplined oscillator
- Low latency PCIE interface



The next generation of wireless networks need to provide enhanced data rates for mobile broadband applications along with low latency and reliability for machine and vehicular communication. Spectrum in conventional cellular frequency bands of 500MHz to 4 GHz is crowded and exorbitantly expensive. For optimum utilization and mass deployment, there are many challenges which need to be addressed such as development of new algorithms, technologies and air interfaces along with energy efficiency and channel models.

The Amitec 5G MASSIVE MIMO LAB is a software programmable hardware transceiver which allows limitless research and development along with laboratory experiments to be performed on a single device. The system is ideally suited for

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applications requiring high RF performance and great bandwidth such as 5G Physical layer prototyping, Dynamic Spectrum Access and Cognitive Radio, Spectrum Monitoring and even Networked Sensor Deployment.

The system has modular architecture with high performance synthesizers and integrated mixers allowing baseband processing. The system has independently programmable transmitter and receiver sections with programmable frequencies, ADC and DAC sampling rates, filter bandwidths, variable gain amplifiers, LNA and PA. The entry barrier is lowered to include the graduate students by providing a graphical programming environment. The burden on faculty is lowered by using courseware designed at IITD from simple FM to most complex MIMO systems. The Ultra Low-latency PCIE interface at 10Gbps serves as the connection between the baseband section and the mobile workstation. This enables the user to realize 10Gbps of real-time throughput in the receive and transmit directions in full duplex mode.

Technical Specifications	
<b>Transmit Frequency Range</b>	50 MHz to 3.8 GHz with 16 Tx channels
<b>Receive Frequency Range</b>	50 MHz to 3.8 GHz with 16 Rx channels
<b>Mode</b>	Full Duplex
<b>Architecture</b>	ASIC/FPGA/Zero IF
<b>Instantaneous Baseband Bandwidth</b>	50 MHz per channel
<b>Frequency Resolution</b>	< 50Hz
<b>Maximum RF Output power</b>	+5dBm
<b>Receiver Sensitivity</b>	-120dBm
<b>TX Output Impedance</b>	50 Ohms
<b>RX Input Impedance</b>	50 Ohms
<b>PLL Phase Noise</b>	-125dBc/Hz at 1MHz
<b>Spurious Output</b>	-50dBc
<b>Transmit Gain Control Range</b>	>50dB
<b>Transmit Gain Control Step</b>	1dB
<b>Rx Noise Figure</b>	<5dB
<b>Rx Gain Control Range</b>	>50dB
<b>Rx Gain Control Step</b>	1dB
<b>IQ Phase Error</b>	3 degree
<b>IQ Amplitude Error</b>	0.5dB
<b>PLL Settling time</b>	<10us
<b>ADC Sample Rate</b>	upto 120 MS/s
<b>DAC Sample Rate</b>	upto 120 MS/s
<b>ADC Resolution</b>	12 bits
<b>DAC Resolution</b>	12 bits
<b>ADC Wideband SFDR</b>	60 dBc
<b>Input Amplitude</b>	1Vp/p differential
<b>DAC Wideband SFDR</b>	60 dBc
<b>Output Amplitude</b>	250mV p/p differential
<b>Frequency Accuracy</b>	50ppb (factory calibrated)
<b>SSB/LO Suppression</b>	>40 dBc
<b>FPGA</b>	Xilinx
<b>Integrated Transceiver</b>	8 Gbps
<b>Logic Elements</b>	40,000
<b>M9K Memory Blocks</b>	>400
<b>Embedded Memory</b>	>2000 Kbits
<b>18bit X18bit Multipliers</b>	>100
<b>PLL</b>	4
<b>Maximum User I/Os</b>	>500
<b>Maximum Channels</b>	>200 Differential
<b>Cable</b>	RG316 SMA-SMA X4,
<b>Antennas</b>	Microstrip Broadband Antenna

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<b>Upgrade</b>	1 year upgrade of new experiments, training videos, operation manuals and firmware	
<b>Shipping List Deliverable</b>	<b>Massive MIMO Setup</b>	<b>1</b>
	<b>Microstrip Broadband Antennas</b>	<b>32</b>
	<b>Manual and Study Material</b>	<b>1</b>

<b>Features</b>	
Software Features: Audio, Boolean, Byte Operators, Channelizers, Channel Models, Coding, Control Port, Debug Tools, Equalizers, Error Coding, File Operators, Filters, Fourier Analysis, GUI Widgets, Impairment Models, Instrumentation, Level Controllers, Math Operators, Measurement Tools, Message Tools, Modulators, Networking Tools, OFDM, Packet Operators, Resamplers, Sinks, Sources, Stream Operators, Stream Tag Tools, Symbol Coding, Synchronizers, Trellis Coding, Type Converters, Variables, Waveform Generators	<p>Synchronizers: Costas Loop, Clock Recovery, Frequency Locked Loop, Phase Locked Loop, Correlate and Sync, Carrier Acquisition</p> <p>Equalizers: Adaptive-CMA, Kurtotic, LMS DD</p> <p>Filters: IIR, FIR, Pulse Shaping-RRC Root Raised Cosine, High Pass, Low Pass, Bandpass, Band Stop, FFT, Frequency Translating Filter</p> <p>Networking: TCP, UDP, Socket, Broadcasting</p>
Channel Coding and Decoding: Convolutional, Viterbi, Trellis	Constellation diagram, Oscilloscope, Spectrum Analyzer and Waterfall display
Analog Channel Models like: Noise: Uniform, Linear, Laplacian, Gaussian, Phase Noise	Spread spectrum techniques like: CSS, DSSS, FHSS, THSS and other variants
Interference: Cross talk, Co-channel, Inter-symbol), Distortion (Inter modulation) Frequency Response (Attenuation and phase shift)	Multiplexing techniques like: TDM, FDM/WDM, SDM, Polarization, Spatial, Packet Switching, MC-SS, OFDM including complete control for new research and development
On-air Transmission and Reception using Analog modulation and Demodulation techniques like: AM, DSBCS, SSB, Narrowband FM, Wideband FM, Stereo FM	On-air Transmission and Reception using Digital Modulation and Demodulation techniques like: ASK, FSK, BPSK, DBPSK, MSK, GMSK, DQPSK, QPSK, OQPSK, pi/4QPSK, 8PSK, 16QAM, 64QAM, 256QAM, CPFSK, GFSK and custom modulation schemes
Channel performance measurements Spectral bandwidth Symbol Rate Bit Rate Channel Capacity Channel Utilization Signal to Noise ratio Bit Error Rate BER Latency and Jitter	<p>Fading modeling: Slow, Fast, Selective/Dispersive, Multipath, Rayleigh, Rician</p> <p>Group delay Propagation Doppler Shift</p>

**Areas of Research and Development**

**mmWave and cmWave Systems**

**Multi-Radio Access Technologies**

**Channel Sounding Measurements**

**Physical Layer Technologies**

**Antenna Design for 5G Cellular Networks**

**Small Cells**

**Heterogeneous Networks**

**Interference Suppression Receivers**

**Medium Access Control Design**

**Dynamic Scheduling of Uplink/Downlink Transmission**

**Cognitive Radio Networks**

**Co-operative Communication**

**Spectrum Sensing and Dynamic Spectrum Allocation**

**New Air Interface and Waveform Design**

**Contextual Awareness**

**Vehicular Communication**

**Low-latency networks**

**Radio Propagation Measurements**

**Channel Modelling**

**Satellite Radio Implementation**

**RADAR Systems**

**Multiple-Hop Networks**

**Machine Type Communication and IoT**

**Dynamic Adhoc Wireless Networks(DAWN)**

**MANET and Wireless Mesh Networks**

**Vandermonde Frequency Division Multiplexing(VFDM)**

**Wireless Network Virtualization**

**Massive MIMO**